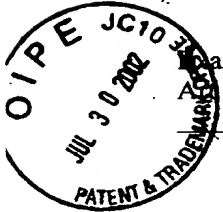


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frederick M. Morgan et al.  
Serial No: 09/675,419  
Confirmation No.: 9659  
Filed: September 29, 2000  
For: SYSTEMS AND METHODS FOR CALIBRATING LIGHT OUTPUT  
BY LIGHT-EMITTING DIODES

COPY OF PAPERS  
ORIGINALLY FILED



Examiner: Kao, Chih-Cheng Glen  
Art Unit: 2882

**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to the Commissioner for Patents, Washington, D.C. 20231, on the 22nd day of July, 2002.

Janine Michalski

OFFICIAL DRAFTSPERSON  
Commissioner for Patents  
Washington, D.C. 20231

**LETTER TO OFFICIAL DRAFTSPERSON**

Sir:

Subject to the approval of the Examiner in this case, enclosed for filing are six (6) sheets of Formal Drawings, Figures 1-6, for the above-referenced patent application.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 23-2825. A duplicate of this sheet is enclosed.

Respectfully submitted,

*Frederick M. Morgan et al., Applicants*

By:

Joseph Teja, Jr., Reg. No. 45,157  
Wolf, Greenfield & Sacks, P.C.  
600 Atlantic Avenue  
Boston, Massachusetts 02210-2211  
Tel. No.: (617) 720-3500  
Attorneys for Applicants

Docket No. C01104/70073  
Date: July 22, 2002

RECEIVED  
AUG - 5 2002  
TECHNOLOGY CENTER 2800

Approved  
CK 10/20/02